

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	14919	programmable adj1 logic adj1 device	US-PGPUB; USPAT	OR	ON	2005/12/13 10:07
L2	4667	1 with (cpu processor)	US-PGPUB; USPAT	OR	ON	2005/12/13 09:59
L3	43	2 with clock	US-PGPUB; USPAT	OR	ON	2005/12/13 09:59
L4	429	1 near4 coupled	US-PGPUB; USPAT	OR	ON	2005/12/13 10:07
L5	160	4 near8 memory	US-PGPUB; USPAT	OR	ON	2005/12/13 10:07
L6	29	5 same (cpu processor)	US-PGPUB; USPAT	OR	ON	2005/12/13 10:18
L7	31	1 with (serial adj1 interface)	US-PGPUB; USPAT	OR	ON	2005/12/13 10:18
S1	15	"6388591"	US-PGPUB; USPAT	OR	ON	2005/12/13 09:58
S2	31	"6407576"	US-PGPUB; USPAT	OR	ON	2005/12/12 12:37
S3	2	"20010033188"	US-PGPUB; USPAT	OR	ON	2005/12/12 12:38
S4	1	"20020190751"	US-PGPUB; USPAT	OR	ON	2005/12/12 12:39
S5	1	"20030052709"	US-PGPUB; USPAT	OR	ON	2005/12/12 12:39
S6	1	"20030155955"	US-PGPUB; USPAT	OR	ON	2005/12/12 12:39
S7	885	(713/500).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/12/12 12:46
S9	708	"3473160" "4486739" "5689195" "5802103" "5909126" "6031428" "6215326" "6240471" "6270350" "6370603"	US-PGPUB; USPAT	OR	ON	2005/12/12 12:47
S10	48	S9 and divider	US-PGPUB; USPAT	OR	ON	2005/12/12 12:48
S11	74924	(multiple plurality) adj2 channels	US-PGPUB; USPAT	OR	ON	2005/12/12 12:49
S12	9837	clock near3 divider	US-PGPUB; USPAT	OR	ON	2005/12/12 12:49
S13	4	S11 with S12	US-PGPUB; USPAT	OR	ON	2005/12/12 12:50
S14	9587	per adj channel	US-PGPUB; USPAT	OR	ON	2005/12/12 12:50
S15	0	S14 with S12	US-PGPUB; USPAT	OR	ON	2005/12/12 12:50
S16	2	S14 same S12	US-PGPUB; USPAT	OR	ON	2005/12/12 12:50
S17	53463	least adj4 channels	US-PGPUB; USPAT	OR	ON	2005/12/12 12:50
S18	90	S14 with S17	US-PGPUB; USPAT	OR	ON	2005/12/12 12:51
S19	51	S18 and @py<"2003"	US-PGPUB; USPAT	OR	ON	2005/12/12 12:51
S20	8	"6708239"	US-PGPUB; USPAT	OR	ON	2005/12/12 15:57
S21	194	(plurality multiple) adj2 (clock adj source)	US-PGPUB; USPAT	OR	ON	2005/12/12 15:57

S22	62	S21 with (select\$3 multiplex\$2)	US-PGPUB; USPAT	OR	ON	2005/12/12 16:53
S23	1919	pld with memory	US-PGPUB; USPAT	OR	ON	2005/12/12 16:57
S24	74924	(multiple plurality) adj2 channels	US-PGPUB; USPAT	OR	ON	2005/12/12 16:53
S25	1	S23 same S24	US-PGPUB; USPAT	OR	ON	2005/12/12 16:54
S26	2407	pld with (cpu processor)	US-PGPUB; USPAT	OR	ON	2005/12/12 16:57
S27	1	S26 same S24	US-PGPUB; USPAT	OR	ON	2005/12/12 16:57
S28	107	S26 same clock	US-PGPUB; USPAT	OR	ON	2005/12/12 16:58
S29	42	S28 same memory	US-PGPUB; USPAT	OR	ON	2005/12/12 17:50
S30	450	generat\$3 adj4 (different adj clock)	US-PGPUB; USPAT	OR	ON	2005/12/12 17:51
S31	0	pld adj5 S30	US-PGPUB; USPAT	OR	ON	2005/12/12 17:51
S32	0	pld with S30	US-PGPUB; USPAT	OR	ON	2005/12/12 17:51
S33	6	pld same S30	US-PGPUB; USPAT	OR	ON	2005/12/12 17:58
S34	785	multiple adj2 (data adj rate)	US-PGPUB; USPAT	OR	ON	2005/12/12 17:58
S35	3	pld with S34	US-PGPUB; USPAT	OR	ON	2005/12/12 17:58